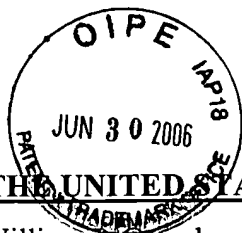


S/N 10/606,868



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: William J. Grundmann  
Serial No.: 10/606,868  
Filed: June 26, 2003  
Title: METHOD AND APPARATUS TO CREATE BYPASS LOGIC IN A DIGITAL CIRCUIT DESIGN  
Assignee: Intel Corporation  
Examiner: Stacy Whitmore  
Group Art Unit: 2825  
Docket No: 884.A38US1  
Customer Number: 21186

PETITION FOR A ONE-MONTH EXTENSION OF TIME

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with the provision of 37 CFR § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Rejection mailed February 28, 2006, said period of response being extended from May 28, 2006 to June 28, 2006.

**Please charge Deposit Account No. 19-0743 in the amount of \$120.00 to cover the required extension fee. Please charge any additional fees or credit overpayment to deposit Account No. 19-0743.**

Respectfully Submitted

WILLIAM J. GRUNDMANN

By his Representatives,

By their Representatives,  
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Attorneys for Intel Corporation  
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07/03/2006 JBALINAM 00000101 190743 10606868

02 FC:1251 120.00 DA

Date: 6-28-06 By: Gregg A. Peacock  
Reg. No: 45,001

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 28th day of June 2006.

Name: Amy Moriarty Signature: [Signature]